

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	5337	first adj processor same second adj processor	USPAT ; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:38
2	BRS	L2	460416	compatibility or compatible	USPAT ; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:01
3	BRS	L3	86	1 same 2	USPAT ; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:01
4	BRS	L4	8632	switch\$3 adj logic	USPAT ; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:01
5	BRS	L5	0	3 and 4	USPAT ; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:01
6	BRS	L6	4971	multiplexed near3 bus	USPAT ; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:01

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	1	3 and 6	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:01
8	BRS	L8	18	(first adj processor near2 type) same (second adj processor near2 type)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:02
9	BRS	L9	669	(first adj processor) same (second adj processor) same interfac\$3	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:17
10	BRS	L10	6	(first adj processor) same (second adj processor) same (control adj interfac\$3)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:18
11	BRS	L11	0	(first adj processor) same (second adj processor) same (iniversal adj interfac\$3)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:18
12	BRS	L12	0	(first adj processor) same (second adj processor) same (universal adj interfac\$3)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:18

	Type	L #	Hits	Search Text	DBs	Time Stamp
13	BRS	L13	0	(first adj processor) same (second adj processor) same (compatible near2 interfac\$3)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:18
14	BRS	L14	37	(first adj processor) same (second adj processor) and (compatible near2 interfac\$3)	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:22
15	BRS	L15	1169	((compatible or universal) near2 interfac\$3).ti,ab.	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:37
16	BRS	L16	4813	motorola same intel	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:22
17	BRS	L17	6	15 and 16	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:33
18	BRS	L18	4	15 and 1	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:34

	Type	L #	Hits	Search Text	DBs	Time Stamp
19	BRS	L19	9	15 and 6	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:36
20	BRS	L20	7	15 and 4	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:36
21	BRS	L21	654	(universal near2 interfac\$3).ti,ab.	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:38
22	BRS	L22	4	16 and 21	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:38
23	BRS	L23	3	1 and 21	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:40
24	BRS	L24	20	6 same 16	USPAT; US-PG PUB; EPO; JPO; DERW ENT; IBM_T DB	2004/03/17 14:40